

CYPR-CD01171M
Serial No. 10/002,726

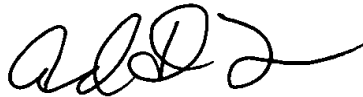
disclosed in the specification). The present amendment intends to clarify references to trademarks of Cypress MicroSystems, Inc. and others (see, e.g., M.P.E.P. § 608.01(v)). No new matter is introduced.

REMARKS

Claims 1-41 are presented for consideration in the present application, which is now believed to be in condition for examination. Early notice to that effect is earnestly solicited.

Respectfully submitted,

WAGNER, MURABITO & HAO LLP

A handwritten signature in black ink, appearing to read 'adD', is written over the printed name of Anthony C. Murabito.

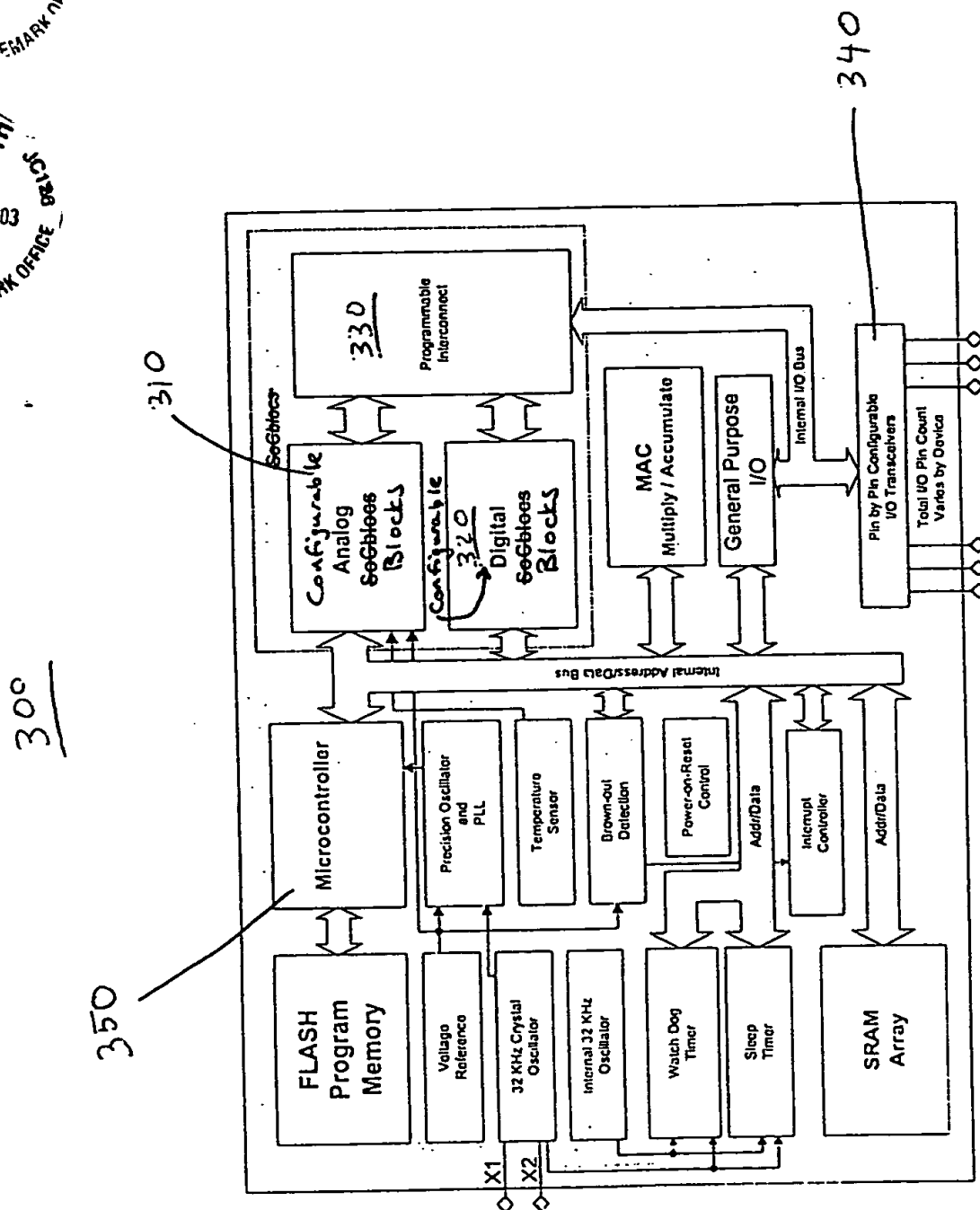
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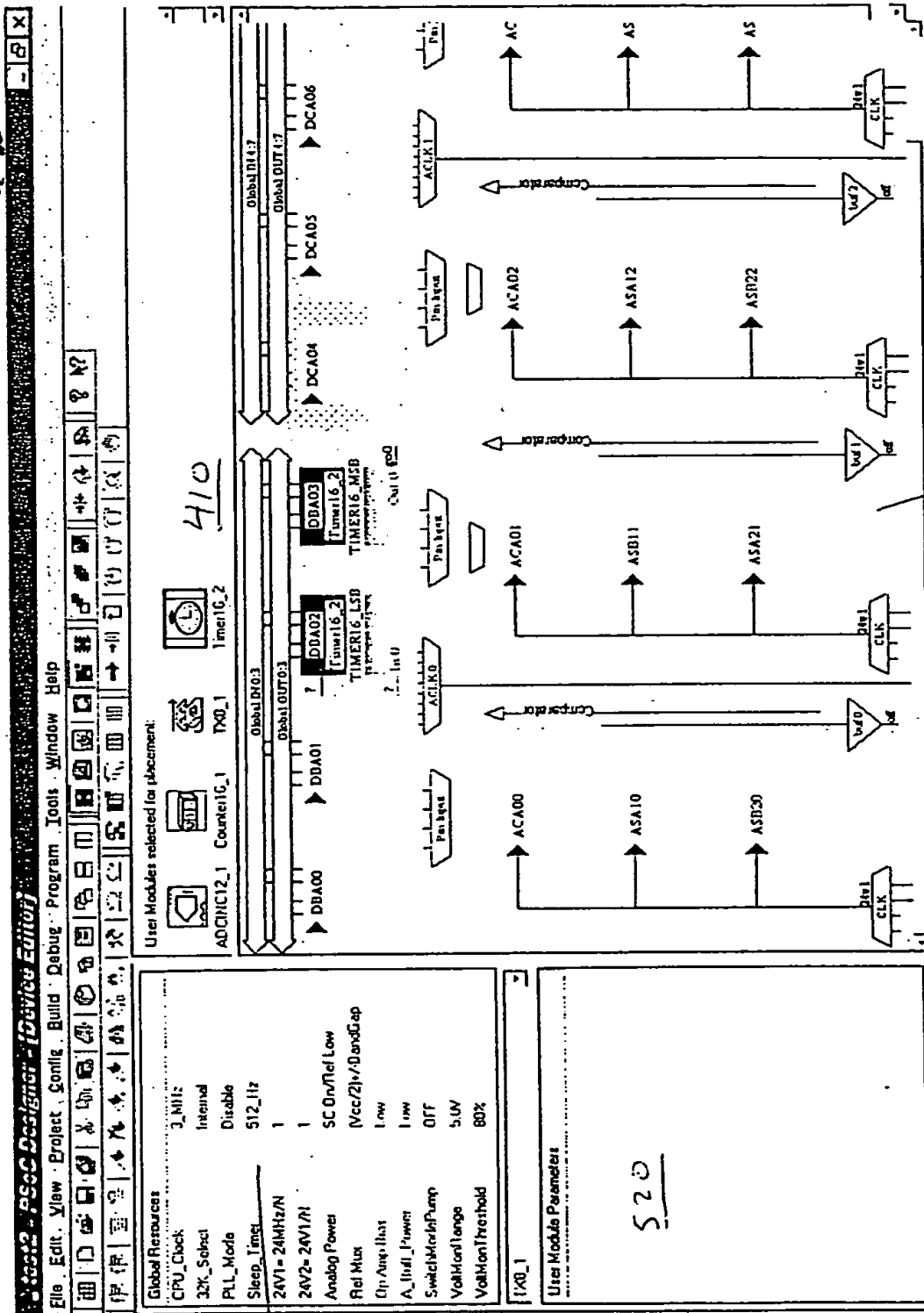


3
1
2
1
4
1

$$\frac{400}{17}$$


Figure 4

500



test2 - PSoc Designer - (Device Editor)
 Document1 - Mi
 2:39 PM

510

FIGURE 5

520

700

```

<!--Project DB Ver 0.03 08/10/00-->
702 - <!--DEVICE_DB>
704 - <!--USER_MODULE_LIST>
      <!--Variable Resolution Incremental ADC-->
706 - <!--USER_MODULE_NAME="ADCEXP" TYPE="PSOC_ADC" HTML="ADCEXP.HTM" ICON="ADC.ico" METAFILE="ADCEXP.cml"
      API_PATH_TYPE="CUSTOM">
707 - <!--SHAPE_SHAPE_TYPE="BLOCKLIST">
708 - <!--BLOCK_LIST>
710 - <!--BLOCK_NAME="ADC" TYPE="ANALOG_SC">
712 - <!--REGISTER_LIST>
714 - <!--REGISTER_NAME="CR0">
716 - <!--BITFIELD_LIST>
718 - <!--BITFIELD_NAME="OSZ" VALUE="OSZ_32"/>
      <!--BITFIELD_NAME="CLOCK_PHASE" VALUE="PHASE_NORM"/>
      <!--BITFIELD_NAME="SIGN" VALUE="POS"/>
      <!--BITFIELD_NAME="CAP_SIZE" VALUE="16"/>
      <!--BITFIELD_LIST>
      <!--REGISTER>
      <!--REGISTER_NAME="CR1">
      <!--BITFIELD_LIST>
      <!--BITFIELD_NAME="CAP_SIZE" VALUE="0"/>
      <!--BITFIELD_LIST>
      <!--REGISTER>
      <!--REGISTER_NAME="CR2">
      <!--BITFIELD_LIST>
      <!--BITFIELD_NAME="A_OUT" VALUE="DISABLED"/>
      <!--BITFIELD_NAME="COMP_OUT" VALUE="ENABLED"/>
      <!--BITFIELD_NAME="AZ_PHASE" VALUE="AZ_PHI"/>

```

Figure 7A





FIGURE 7E

```
<API_REGISTER_ALIAS NAME="CounterMSBSL" SOURCE="CNT_MSB" REGISTER_NAME="DIG_Input"/>
<API_REGISTER_ALIAS NAME="CounterMSBOS" SOURCE="CNT_MSB" REGISTER_NAME="DIG_Output"/>
<API_REGISTER_ALIAS NAME="CounterMSBDR0" SOURCE="CNT_MSB" REGISTER_NAME="DATA_0"/>
<API_REGISTER_ALIAS NAME="CounterMSBDR1" SOURCE="CNT_MSB" REGISTER_NAME="DATA_1"/>
<API_REGISTER_ALIAS NAME="CounterMSBDR2" SOURCE="CNT_MSB" REGISTER_NAME="DATA_2"/>
<API_REGISTER_ALIAS NAME="CounterMSBCR0" SOURCE="CNT_MSB" REGISTER_NAME="CONTROL_0"/>
<API_REGISTER_ALIAS NAME="CounterLSBSL" SOURCE="CNT_LSB" REGISTER_NAME="DIG_BasicFunction"/>
<API_REGISTER_ALIAS NAME="CounterLSBOS" SOURCE="CNT_LSB" REGISTER_NAME="DIG_Output"/>
<API_REGISTER_ALIAS NAME="CounterLSBDR0" SOURCE="CNT_LSB" REGISTER_NAME="DATA_0"/>
<API_REGISTER_ALIAS NAME="CounterLSBDR1" SOURCE="CNT_LSB" REGISTER_NAME="DATA_1"/>
<API_REGISTER_ALIAS NAME="CounterLSBDR2" SOURCE="CNT_LSB" REGISTER_NAME="DATA_2"/>
<API_REGISTER_ALIAS NAME="CounterLSBCR0" SOURCE="CNT_LSB" REGISTER_NAME="CONTROL_0"/>
<API_REGISTER_ALIAS NAME="TimerIN" SOURCE="TMR" REGISTER_NAME="DIG_BasicFunction"/>
<API_REGISTER_ALIAS NAME="TimerSL" SOURCE="TMR" REGISTER_NAME="DIG_Input"/>
<API_REGISTER_ALIAS NAME="TimerOS" SOURCE="TMR" REGISTER_NAME="DIG_Output"/>
<API_REGISTER_ALIAS NAME="TimerDR0" SOURCE="TMR" REGISTER_NAME="DATA_0"/>
<API_REGISTER_ALIAS NAME="TimerDR1" SOURCE="TMR" REGISTER_NAME="DATA_1"/>
<API_REGISTER_ALIAS NAME="TimerDR2" SOURCE="TMR" REGISTER_NAME="DATA_2"/>
<API_REGISTER_ALIAS NAME="TimerCR0" SOURCE="TMR" REGISTER_NAME="CONTROL_0"/>
<API_REGISTER_ALIAS NAME="Comp_Ctrl" SOURCE="AnalogComp_Type" TYPE="RESERVED_RESOURCE"/>
<API_REGISTER_ALIAS_LIST>
</API_REGISTER_ALIAS_LIST>
</USER_MODULE_LIST>
</FreeDevice_DB>
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